

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2/A
af
2/27

In re the Application of:
Iain Robertson, et al.
Serial No: TBD
Filed: Herewith

Docket No.: TI-28925
Art Unit: TBD
Examiner: TBD

For: Communications Interface Between Clock Domains with Minimal Latency

Assistant Commissioner for
Patents
Washington, DC 20231

"EXPRESS MAILING" Mailing Label No.
EL547744725US. I hereby certify that this paper is being
deposited with the U.S. Postal Service Express Mail Post
Office to Addressed Service under 37 CFR 1.10 on the
date shown below and is addressed to the Assistant
Commissioner for Patents, Washington, D.C. 20231.

J. Dennis Moore, Reg. No. 28,885

January 5, 2001
Date

PRELIMINARY AMENDMENT FOR PROVISIONAL PRIORITY

Sir:

This application claims priority under 35 USC 119 (e)(1) of provisional application number
60/185,802, filed 02/29/00.

Please amend the specification by inserting before the first line the sentence:

This application claims priority under 35 U.S.C. § 119(e)(1) of provisional application
number 60/185,802 filed 02/29/00.

Respectfully submitted,

J. Dennis Moore
Attorney for Applicants
Reg. No. 28,885

Texas Instruments Incorporated
Mail Station 3999
P.O. Box 655474
Dallas, Texas 75265
(972) 917-5646